



# PRODUCT INFORMATION LETTER

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PIL MMS-MIC/14/8569  
Dated 10 Jul 2014

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**STM32F42x and STM32F43x family Errata sheet update**

Sales Type/product family label	STM32F42x and STM32F43x products listed below
Type of change	Product electrical spec. change
Reason for change	Product limitation described in Errata sheet update
Description	STM32F42x and STM32F43x Errata sheet is updated to include a product limitation.
Forecasted date of implementation	10-Aug-2014
Forecasted date of samples for customer	03-Jul-2014
Forecasted date for <b>STMicroelectronics</b> change Qualification Plan results availability	03-Jul-2014
Involved ST facilities	not applicable

## DOCUMENT APPROVAL

Name	Function
Colonna, Daniel	Marketing Manager
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## PRODUCT INFORMATION LETTER

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### STM32F42x and STM32F43x family Errata sheet update

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#### MMS - Microcontrollers Division (MCD)

Dear Customer,

We wish to inform you that the STM32F42x and STM32F43x Errata sheet is updated to include a product limitation.

#### What is the change?

When PA12 is used as GPIO or alternate function in input or output mode, the data read from Flash memory can be corrupted. This behavior is only observed when the following conditions are met:

- The device operates from a 2.7 to 3.6 V VDD power supply whatever the temperature range.
- Flash memory Bank2 is used or dual bank feature enabled.

Impacted products (listed below):

- STM32F42xxI & STM32F43xxI part numbers.
- STM32F42xxG & STM32F43xxG part numbers only when dual bank feature is enabled

Not impacted products:

- STM32F42xxG & STM32F43xxG part numbers when dual bank feature is disabled
- STM32F42xxE & STM32F43xxE part numbers

**Workaround**

PA12 must be left unconnected on the PCB (configured as push-pull and held Low). You can use all the other GPIOs and all alternate functions except for the ones mapped on PA12. Use the OTG\_HS peripheral in full-speed mode instead of the OTG\_FS peripheral.

This limitation is fixed in silicon revision 3.

We remain available to discuss any concern you may have regarding this product information.

With our sincere regards.

Michel Buffa

Microcontroller Division General Manager

**List of commercial products impacted:**

STM32F427IGH6	STM32F439BGT6
STM32F427IG T6	STM32F439BIT6
STM32F427IIH6	STM32F439BIT6U
STM32F427IIH6TR	STM32F439IGH6
STM32F427IIH6U	STM32F439IG T6
STM32F427IIH7	STM32F439IIH6
STM32F427IIT6	STM32F439IIH6TR
STM32F427IIT6TR	STM32F439IIH6U
STM32F427VGT6	STM32F439IIT6
STM32F427VIT6	STM32F439IIT6TR
STM32F427VIT6TR	STM32F439IIT6U
STM32F427ZGT6	STM32F439NGH6
STM32F427ZIT6	STM32F439NIH6
STM32F429BGT6	STM32F439NIH6U
STM32F429BIT6	STM32F439VGT6
STM32F429BIT6U	STM32F439VIT6
STM32F429IGH6	STM32F439VIT6U
STM32F429IG T6	STM32F439ZGT6
STM32F429IIH6	STM32F439ZIT6
STM32F429IIH6TR	STM32F439ZIT6U
STM32F429IIH6U	STM32F439ZIY 6G TR
STM32F429IIT6	STM32F439ZIY 6S TR
STM32F429IIT6U	STM32F439ZIY 6TR
STM32F429NGH6	STM32F439ZIY 6UTR
STM32F429NIH6	
STM32F429NIH6U	
STM32F429VGT6	
STM32F429VIT6	
STM32F429VIT6U	
STM32F429ZGT6	
STM32F429ZGY6TR	
STM32F429ZIT6	
STM32F429ZIT6U	
STM32F429ZIY 6TR	
STM32F429ZIY 6UTR	
STM32F437IGH6	
STM32F437IG T6	
STM32F437IIH6	
STM32F437IIH6TR	
STM32F437IIH6U	
STM32F437IIT6	
STM32F437VGT6	
STM32F437VIT6	
STM32F437VIT6TR	
STM32F437VIT6WTR	
STM32F437ZGT6	
STM32F437ZIT6	
STM32F437ZIT7	
STM32F437ZIT7	

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